

Appln No. 09/651,425

Amdt date July 9, 2003

Reply to Office action of April 10, 2003

REMARKS/ARGUMENTS

Claims 1-44 are currently pending in this application. Claims 1, 22, 43 and 44 have been amended to place them in better condition for allowance. In view of the above amendment and following remarks, applicant respectfully submits that the application is in condition for allowance. Applicants therefore, respectfully request reconsideration and allowance of the application.

The Examiner objected to the drawings on various informal grounds. Appropriate corrected formal drawings are submitted herewith under separate cover to replace the corresponding informal drawings as originally filed.

The Examiner rejected claims 1-9, 19-30 and 41-44 under 35 U.S.C. 103(a) as being obvious over Tseng et al. (U.S. Patent 6,009,256) in view of Kolchinsky et al. (U.S. Patent 5,535,406). Applicants respectfully traverse this rejection.

Independent claims 1, 43 and 44 recite in part "decomposing a program source code into a plurality of kernel sections for execution on said hardware accelerators..." Applicant respectfully submits that the cited references alone or in combination do not disclose or suggest the recited limitations.

Rather Tseng is directed towards electronic design automation (EDA) systems and software. According to Tseng, electronic design automation (EDA) is a computer-based tool configured to provide designers with automated or semi-automated tools for designing and verifying user's custom circuit designs. EDA is generally used for creating, analyzing, and editing any

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electronic design for the purpose of simulation, emulation, prototyping, execution, or computing. (Tseng, col. 1, lines 13-19).

For example, Tseng discloses that to "run the simulation and emulation, a model of the circuit design and the relevant parameters (e.g., input test-bench stimulus, overall system output, intermediate results) are determined and provided to the simulation software system... The user starts with a circuit design of an electronic system, usually in draft schematic form, which is then converted to HDL form using synthesis tools." (Tseng, col. 13, lines 21-29). Thus, in Tseng an electronic design is converted into a software description which allows a user to "verify the accuracy of the circuit through simulation by applying various stimuli such as input signals and test vector patterns to the simulated model. If, during the simulation, the circuit does not behave as planned, the user re-defines the circuit by modifying the circuit schematic or the HDL file." (Tseng, col. 13, lines 41-47).

Tseng, converts an electronic circuit design into a software model and simulates / emulates the performance of that circuit to improve the circuit design. Tseng does not however, decompose a program source code into a plurality of kernels for execution on a plurality of hardware accelerators as recited in claims 1, 43 and 44 of the present invention. Applicants therefore respectfully submit that independent claims 1, 43 and 44 are novel and unobvious over the cited references and are therefore allowable. Applicants further submit that claims 2-21

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that depend directly or indirectly from claim 1 are allowable as is claim 1 and for additional limitations recited therein.

Similarly, independent claim 2 recites system for creating run time executable code comprised in part by "a plurality of kernel sections created from a program source code for execution on said plurality of hardware accelerators..." Applicants respectfully submit that the cited reference alone or in combination do not disclose or suggest the recited limitations.

Rather, as argued above with respect to claims 1, 43 and 44 Tseng does not create a plurality of kernels from a program source code for execution on a plurality of hardware accelerators as recited in claim 22. Therefore, applicants respectfully submit that claim 22 recites a novel and unobvious apparatus over the prior art and is therefore allowable. Applicants further submit that claims 23-42 that depend directly or indirectly from claim 22 are allowable as is claim 22 and for additional limitations recited therein.

Respectfully submitted,
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